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The invention relates to the electromeasuring engineering.

The purpose of the invention is the stepping-up of the upper limit and the precision increase.

The electric resistance and conductance simulator contains a Kelvin-Warlay digital voltage divider 6, a microprocessor control processing unit 12, an operational amplifier 7, the first and the second current terminals 1, 2, the first and the second potential terminals 4, 5.

With the advent of the third current terminal 3, the resistance boxes 8-10, the data control channel 11, the visual display 13, the first and the second controllable multiresistors 14 and 15, the data control channels 16 and 17 the upper limit will be increased and will be stepped-up the precision.

Claims: 1

Fig.: 2