

The invention relates to the computer engineering and microelectronics, and may be used for the production and exploitation of superintegrated circuits, provided with compact testing and diagnosticating means.

The device with reset input 6 and beat input 7 contains a synchronizing register 1, a counter 2, on-line memory 3, a modulo  $q$  summer 4, a group of registers 5, a commutator 8, a logic unit AND 9, three logic units OR 10, 11, 12.