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The invention relates to the field of electrical engineering, in particular to inverters made on the basis of transistors. The bridge inverter comprises a voltage input terminal (U), connected to the positive pole of a capacitor (C1), the drains of field-effect transistors (Q1, Q2) and to the input of a power supply (1), the output of which is connected to the inputs of a pulse generator (2) and of drivers (3, 4), while the outputs of the pulse generator (2) are connected, respectively, to the inputs of the drivers (3, 4), a voltage output terminal (U/2), connected to the positive pole of a capacitor (C2), and to the middle lead of a power transformer (Tr), consisting of two identical windings (W1, W2), while the lower lead of the power supply (1) is connected to the lower lead of the pulse generator (2), the lower leads of the drivers (3, 4), one of the outputs of the drivers (3, 4), sources of field-effect transistors (Q3, Q4), negative poles of the capacitors (C1, C2) and to a common terminal (COM). The drain of the field-effect transistor (Q3) is connected to the lead of the winding (W2), one output of the driver (4) and to the source of the field-effect transistor (Q2), the drain of the field-effect transistor (Q4) is connected to the lead of the winding (W1), one output of the driver (3) and to the source of the field-effect transistor (Q1). At the same time, the other two outputs of the driver (3) are connected to the gate of the field-effect transistor (Q1) and the gate of the field-effect transistor (Q3), respectively, and the other two outputs of the driver (4) are connected to the gate of the field-effect transistor (Q2) and the gate of the field-effect transistor (Q4), respectively.

Claims: 1
Fig.: 1